IN THE CLAIMS:

Please amend claims 1-10 as follows:

1. (Currently Amended) A method for forming interlayer connections in a layered electronic device~~, particularly a thin-film device~~ for storing or processing of data, wherein the device comprises electrical connections between circuitry located in two or more circuit layers separated by layers of electrically insulating material, wherein conducting material is applied as current paths on each circuit layer for connecting the circuitry located therein and joined with interlayer connections consisting of plugs or wires of highly electrically conducting material penetrating said interlayers of electrically insulating material, wherein the plugs or wires in the plane of said interlayers have a cross section with dimensions that are longer in one direction, with a longitudinal dimension of representative magnitude Y and a transversal dimension of representative magnitude X, such that Y>X, and ~~wherein~~ the method ~~is characterized by~~ comprising the step of

forming a plug or wire in one and the same step as used for applying the conducting material for a conducting path on an overlying circuit layer.

2. (Currently Amended) ~~A~~ The method according to claim 1, ~~characterized by providing~~ wherein said plug or wire is provided for connecting current paths in the form of at least one narrow stripe electrode in one or more circuit layers, and in case of more than one, providing all stripe electrodes oriented in parallel, and by orienting said plug or wire such that its longitudinal dimension becomes parallel to the longitudinal direction of said at least one stripe electrode.

3. (Currently Amended) ~~A~~ The method according to claim 2, ~~characterized by forming~~ wherein said plug or wire is formed completely contained within a footprint of said at least one stripe electrode.

4. (Currently Amended) ~~A~~ The method according to claim 1, ~~characterized by forming~~ wherein said plug or wire is formed with a ratio between the longitudinal and transversal dimensions, Y ~~resp.~~ and X, respectively, such that $Y/X \geq 2.5$.

5. (Currently Amended) ~~A~~ The method according to claim 1, ~~characterized by forming~~ wherein said plug or wire is formed with the end sides along the short dimension tapering outwards towards the overlying circuit layer.

6. (Currently Amended) An interlayer connection in a layered electronic device ~~, particularly~~ for storing or processing of data, wherein the device comprises electrical connections between circuitry located in two or more circuit layers separated by layers of electrically insulating material, wherein conducting material is applied as current paths on each circuit layer for connecting the circuitry located therein and joined with interlayer connections consisting of plugs or wires of highly electrically conducting material penetrating said interlayers of electrically insulating material, wherein the plugs or wires in the plane of said interlayers have a cross section with dimensions that are longer in one direction, with a longitudinal dimension of representative magnitude Y and a transversal dimension of representative magnitude X, such that Y>X,

~~characterized in that~~ said interlayer connection comprising the plug or wire ~~is~~ being provided integral with the conducting material of a conducting path on an overlying circuit layer.

7. (Currently Amended) ~~An~~ The interlayer connection according to claim 6,

~~characterized in that~~ wherein current paths are provided in a layer as narrow parallel stripe electrodes, and ~~that~~ the cross section of the plug or wire is provided with its long dimension

parallel to the longitudinal direction of the connected stripe electrode on the overlying circuit layer.

8. (Currently Amended) ~~An~~ The interlayer connection according to claim 7,

~~characterized in that~~ wherein said plug or wire is completely contained within a footprint of at least one stripe electrode.

9. (Currently Amended) ~~An~~ The interlayer connection according to claim 6,

~~characterized in that~~ wherein said plug or wire have a ratio between the long and short dimension Y ~~resp.~~ and X , respectively, such that $Y/X \geq 2.5$.

10. (Currently Amended) ~~An~~ The interlayer connection according to claim 6,

~~characterized in that~~ wherein said plug and wire are provided with the end sides along the short dimension tapering outward towards the overlying circuit layer.